FIG. 1

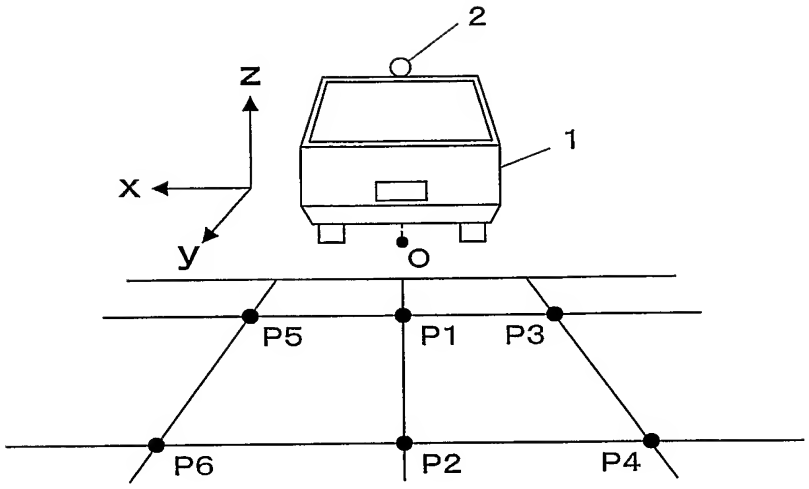


FIG. 2

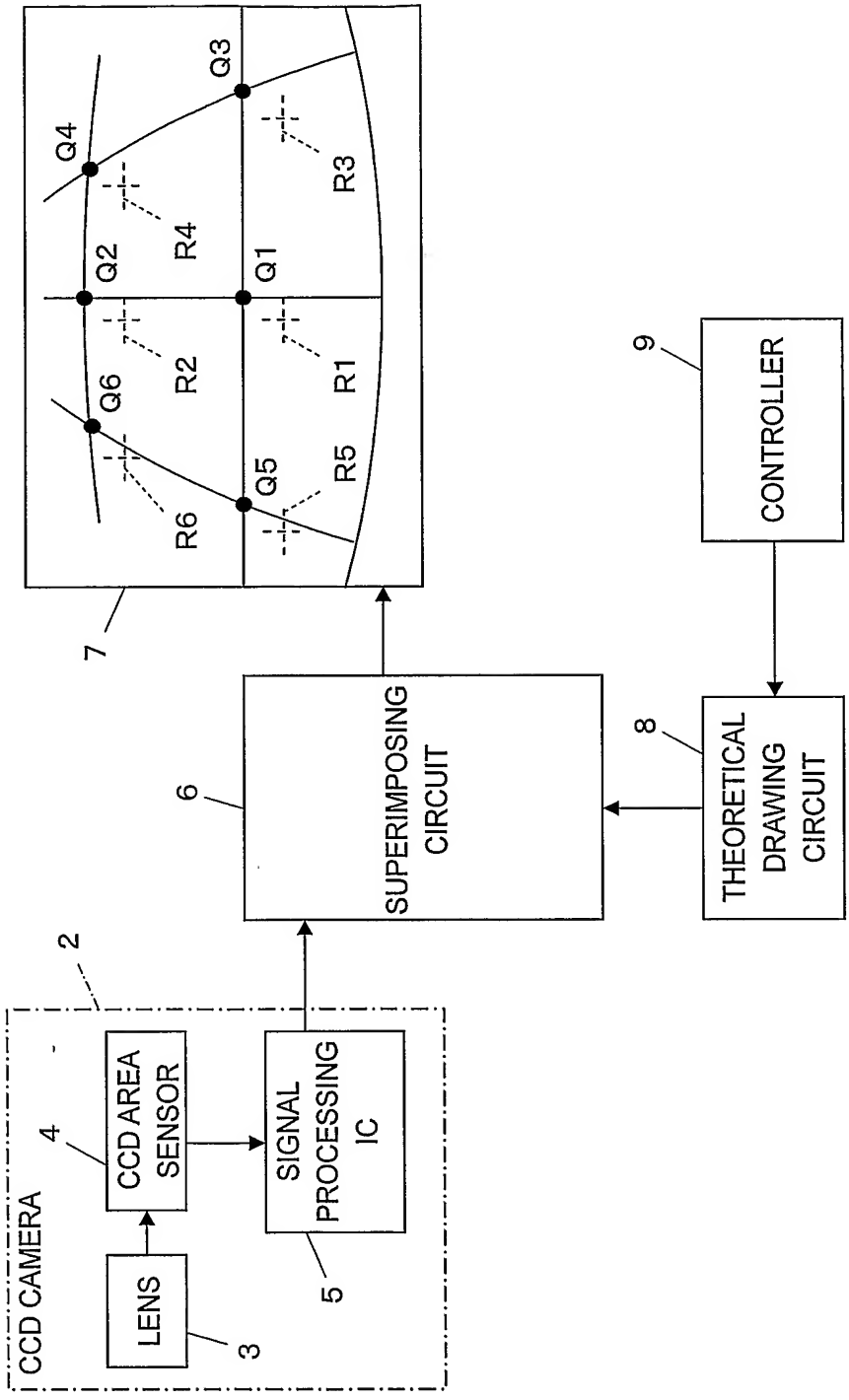

CCD CAMERA
2
LENS
3
CCD AREA SENSOR
4
SIGNAL PROCESSING IC
5
SUPERIMPOSING CIRCUIT
6
7
Q1
Q2
Q3
Q4
Q5
Q6
R1
R2
R3
R4
R5
R6
THEORETICAL DRAWING CIRCUIT
8
CONTROLLER
9

FIG. 3

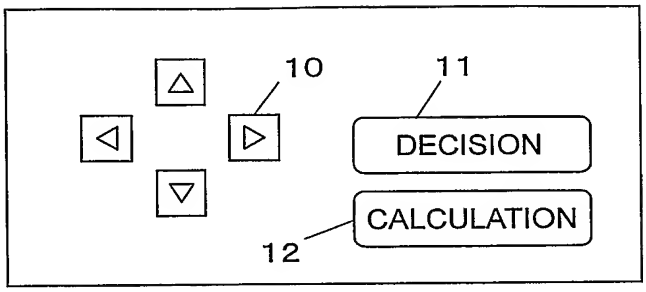


FIG. 4

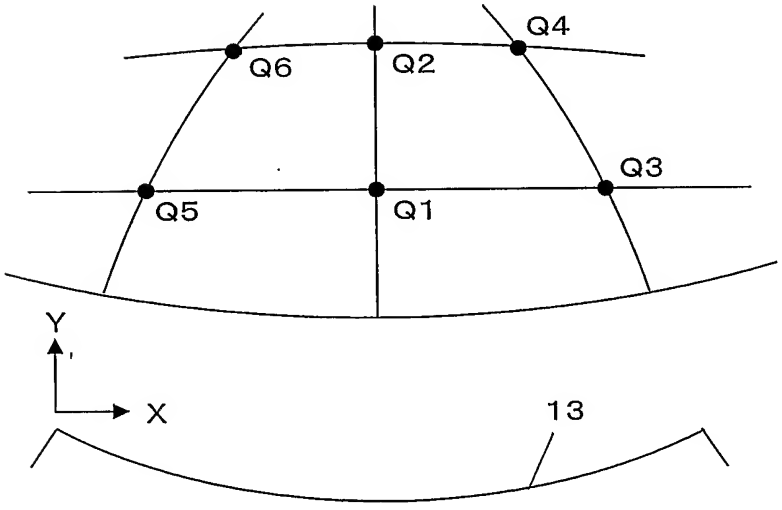


FIG. 5

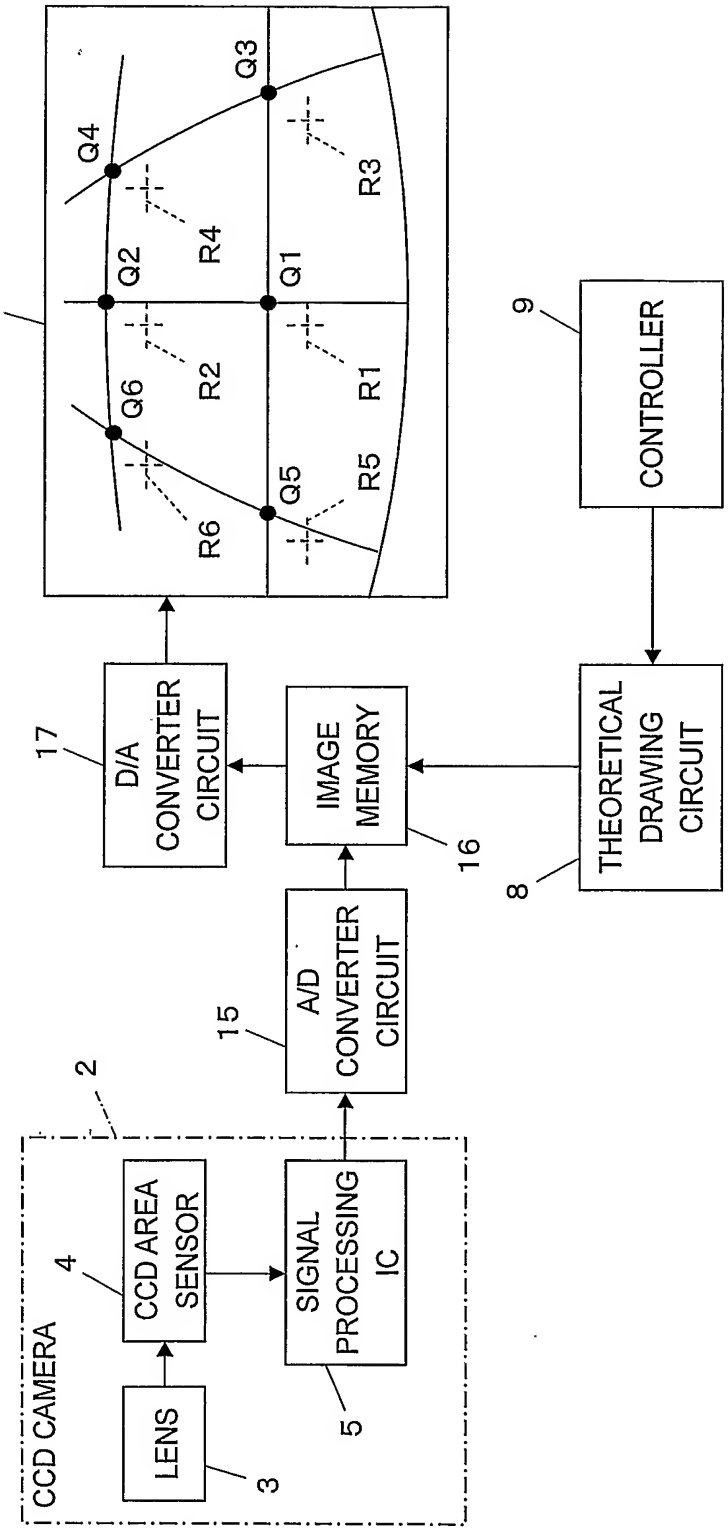
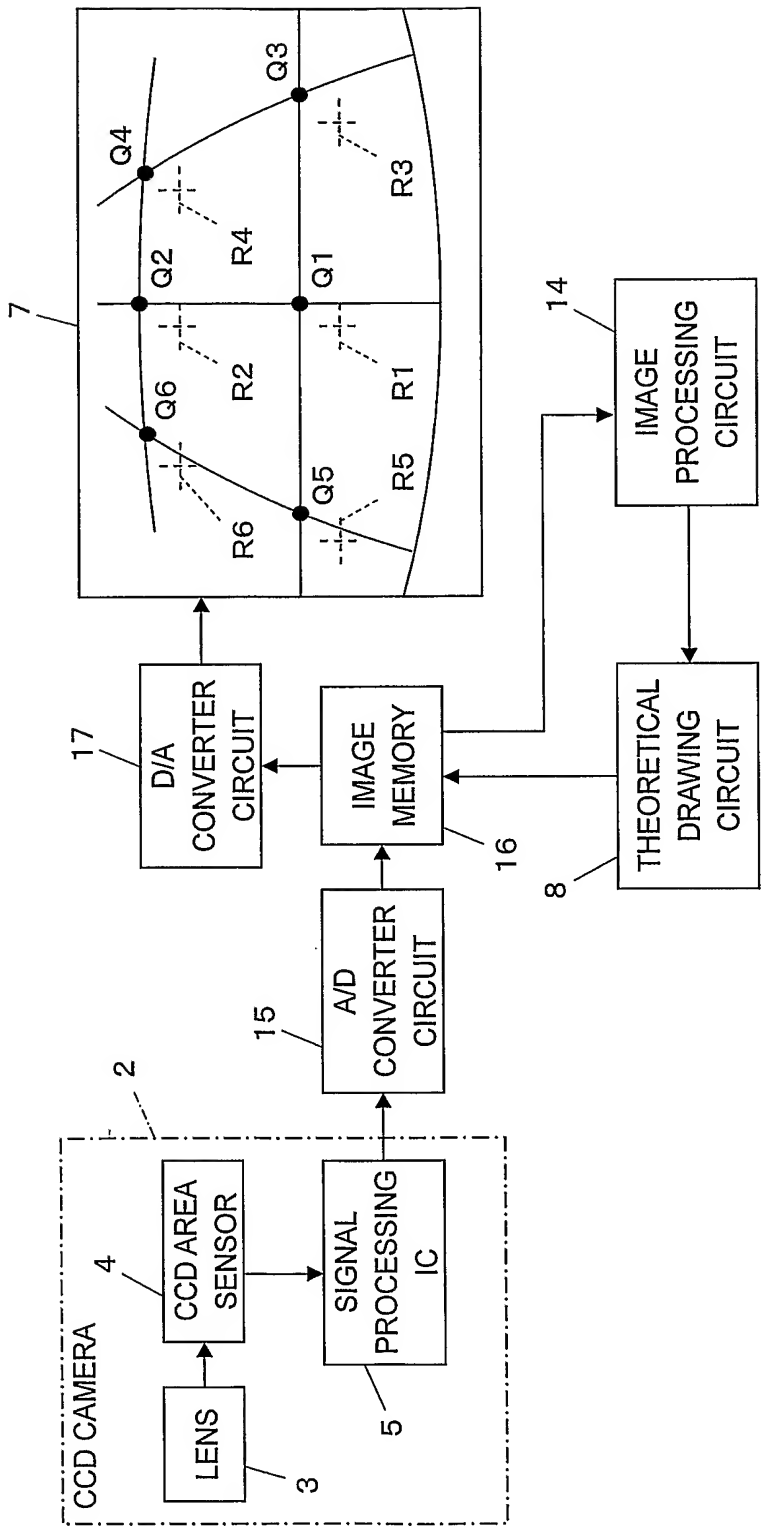

CCD CAMERA
LENS
3
CCD AREA SENSOR
4
SIGNAL PROCESSING IC
5
2
A/D CONVERTER CIRCUIT
15
IMAGE MEMORY
16
D/A CONVERTER CIRCUIT
17
7
Q1
Q2
Q3
Q4
Q5
Q6
R1
R2
R3
R4
R5
R6
THEORETICAL DRAWING CIRCUIT
8
CONTROLLER
9

FIG. 6


CCD CAMERA
2
LENS
3
CCD AREA SENSOR
4
SIGNAL PROCESSING IC
5
A/D CONVERTER CIRCUIT
15
IMAGE MEMORY
16
D/A CONVERTER CIRCUIT
17
7
Q1
Q2
Q3
Q4
Q5
Q6
R1
R2
R3
R4
R5
R6
IMAGE PROCESSING CIRCUIT
14
THEORETICAL DRAWING CIRCUIT
8

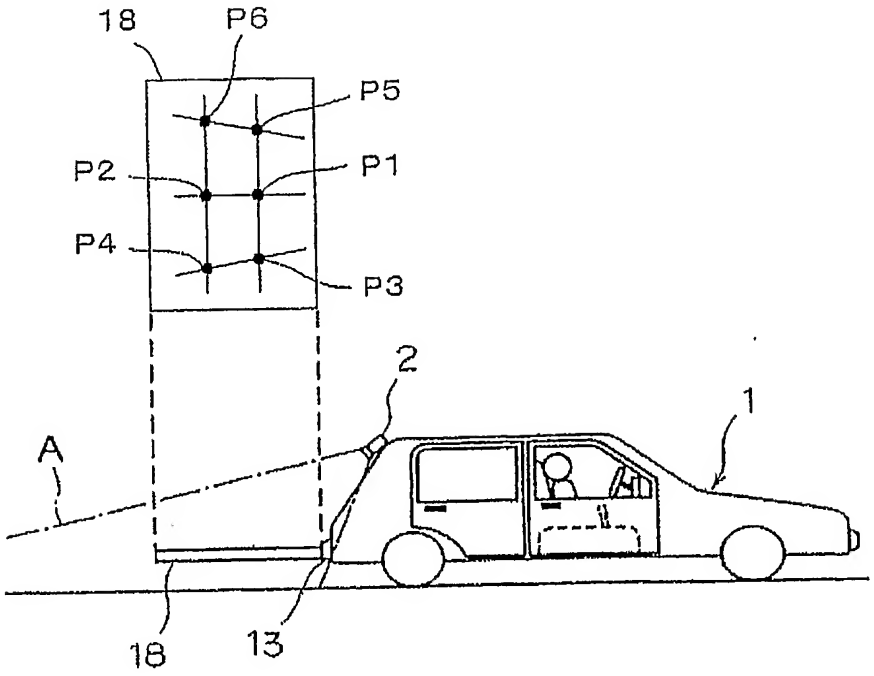



FIG. 7